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APPLICANTS Joachim Brilka, Hamburg, GERMANY; Axel Kattner, Seevetal, GERMANY; Ernst-Peter Ragosche, Stade, GERMANY;				
** CONTINUING DATA ***** This application is a 371 of PCT/IB04/00752 03/16/2004				
** FOREIGN APPLICATIONS ***** EUROPEAN PATENT OFFICE (EPO) 03100720.6 03/20/2003				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/11/2006				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>[Signature]</i> <i>[Signature]</i> Examiner's Signature Initials		STATE OR COUNTRY GERMANY	SHEETS DRAWING 2	TOTAL CLAIMS 6
				INDEPENDENT CLAIMS 2
ADDRESS 24737				
TITLE Circuit arrangement and transistor control method				
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	